

AMENDMENT TRANSMITTAL LETTER (Large Entity)

Applicant(s): Darren L. Anand, et al.

Docket No.

BUR920030168US1 (17124)

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/707,071	November 19, 2003	James C. Kerveros	23389	2138	1070

Invention: AUTOMATIC BIT FAIL MAPPING FOR EMBEDDED MEMORIES WITH CLOCK MULTIPLIERS

COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	25 -	22 =	3	x \$50.00	\$150.00
INDEP. CLAIMS	4 -	4 =	0	x \$200.00	\$0.00
Multiple Dependent Claims (check if applicable)		<input type="checkbox"/>			\$0.00
					\$150.00

No additional fee is required for amendment.

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Steven Fischman
Registration No. 34,594

Scully, Scott, Murphy & Presser, P.C.
400 Garden City Plaza - Suite 300
Garden City, New York 11530
(516) 742-4343

Dated: July 27, 2007

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